

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1125	cycl\$3 same switch\$3 same tandem	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 20:54
L2	1	cycl\$3 same switch\$3 same tandem same pre\$charge	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 20:54
L3	2	cycl\$3 same switch\$3 same tandem and pre\$charge	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 20:56
L4	54	cycl\$3 same switch\$3 same tandem same protect\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 21:02
L11	14	cycl\$3 same switch\$3 same tandem same protect\$3 same capacitor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 21:12
L12	17	cycl\$3 same (switch\$3 transistor fet mos contactor) same tandem same protect\$3 same capacitor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 21:13
L13	3	12 not 11	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 21:13
L14	63	cycl\$3 same (switch\$3 transistor fet mos contactor) same tandem same protect\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 21:23

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L15	65	(cycl\$3 synch) same (switch\$3 transistor fet mos contactor) same tandem same protect\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 21:25
L16	2	15 not 14	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 21:24
L17	75	(cycl\$3 synchron\$3) same (switch\$3 transistor fet mos contactor) same tandem same protect\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 21:25
L18	12	17 not 15	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 21:25
L19	3567833	(fault\$3 error defect\$3 deficien\$3 imperfec\$3 weak\$4 insufficien\$3 inadequa\$3 lack\$3 miss\$3 neglect\$3 overlook\$3 wrongdo\$3 wrong\$2 inaccura\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 21:33
L20	19318466	(sens\$3 detect\$3 monitor\$3 measur\$3 spot\$4 distinguish\$3 examin\$3 inspect\$3 read\$3 recogniz\$3 spy\$3 study\$3 view\$3 watch\$3 audit\$3 catch\$3 observ\$3 analiz\$3 compar\$3 investigat\$3 control\$4 scan\$4 supervis\$3 track\$3 encounter\$3 ideintify\$3 notic\$3 reveal\$3 (track\$3 ADJ down) uncover\$3 unmask\$3 discover\$3 (figur\$3 ADJ out) determin\$3 differentiat\$3 dig\$4 discriminat\$3 perceiv\$3 metrology)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 21:33
L21	184	(L19 adj L20) and (temperature near3 temporarily)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 21:38

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L24	1	(L19 adj L20) and (temperature near3 switch near3 temporarily)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 21:51
L25	458129	L19 near L20	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 21:53
L26	5	L25 and (temperature near5 (switch transistor mos fet contact) near5 temporarily)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 21:55

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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S10	33821	(fault adj detect\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 11:28
S11	3	S10 same (float\$3 near3 power near3 transfer\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 11:22
S12	3	S10 same (float\$3 near5 power near5 transfer\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 11:22
S13	3	S10 same (float\$3 near7 power near7 transfer\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 11:23
S14	3	S10 same (float\$3 near10 power near10 transfer\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 11:23
S16	6	S10 same (float\$3 same power same transfer\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 11:23
S17	36160	(fault adj (monitor\$3 detect\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 11:28
S18	3567833	(fault\$3 error defect\$3 deficien\$3 imperfec\$3 weak\$4 insufficien\$3 inadequa\$3 lack\$3 miss\$3 neglect\$3 overlook\$3 wrongdo\$3 wrong\$2 inaccura\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 11:53

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S19	19318466	(sens\$3 detect\$3 monitor\$3 measur\$3 spot\$4 distinguish\$3 examin\$3 inspect\$3 read\$3 recogniz\$3 spy\$3 study\$3 view\$3 watch\$3 audit\$3 catch\$3 observ\$3 analiz\$3 compar\$3 investigat\$3 control\$4 scan\$4 supervis\$3 track\$3 encounter\$3 ideintify\$3 notic\$3 reveal\$3 (track\$3 ADJ down) uncover\$3 unmask\$3 discover\$3 (figur\$3 ADJ out) determin\$3 differentiat\$3 dig\$4 discriminat\$3 perceiv\$3 metrology)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 11:53
S20	458129	S18 near S19	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 21:52
S21	285673	S18 adj S19	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 21:32
S23	3	S21 same (float\$3 near3 power near3 transfer\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 12:12
S24	5	S21 and (float\$3 near3 power near3 transfer\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 12:11
S25	3	S20 same (float\$3 near3 power near3 transfer\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 12:15
S26	3	S20 same (float\$10 near10 power near10 transfer\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 12:16

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S27	43	S20 same (float\$10 same power same transfer\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 12:17
S28	25	S27 same (capacitor battery stor\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 12:39
S29	35	S27 and (capacitor battery stor\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 12:47
S30	3	S27 and (capacitor battery stor\$3) and (pre\$charg\$3 and reservoir)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 12:49
S31	6	S27 and (capacitor battery stor\$3) and (pre\$charg\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 14:21
S33	7	S27 and (capacitor battery stor\$3) and (reservoir)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 12:50
S34	3567833	(fault\$3 error defect\$3 deficien\$3 imperfec\$3 weak\$4 insufficien\$3 inadequa\$3 lack\$3 miss\$3 neglect\$3 overlook\$3 wrongdo\$3 wrong\$2 inaccura\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 14:22

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S35	19318466	(sens\$3 detect\$3 monitor\$3 measur\$3 spot\$4 distinguish\$3 examin\$3 inspect\$3 read\$3 recogniz\$3 spy\$3 study\$3 view\$3 watch\$3 audit\$3 catch\$3 observ\$3 analiz\$3 compar\$3 investigat\$3 control\$4 scan\$4 supervis\$3 track\$3 encounter\$3 ideintify\$3 notic\$3 reveal\$3 (track\$3 ADJ down) uncover\$3 unmask\$3 discover\$3 (figur\$3 ADJ out) determin\$3 differentiat\$3 dig\$4 discriminat\$3 perceiv\$3 metrology)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 14:22
S36	458129	S34 near S35	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 14:22
S37	43	S36 same (float\$10 same power same transfer\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 14:22
S38	6	S37 and (capacitor battery stor\$3) and (pre\$charg\$3 precharg\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 14:28
S39	3567833	(fault\$3 error defect\$3 deficien\$3 imperfec\$3 weak\$4 insufficien\$3 inadequa\$3 lack\$3 miss\$3 neglect\$3 overlook\$3 wrongdo\$3 wrong\$2 inaccura\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 15:40
S40	19318466	(sens\$3 detect\$3 monitor\$3 measur\$3 spot\$4 distinguish\$3 examin\$3 inspect\$3 read\$3 recogniz\$3 spy\$3 study\$3 view\$3 watch\$3 audit\$3 catch\$3 observ\$3 analiz\$3 compar\$3 investigat\$3 control\$4 scan\$4 supervis\$3 track\$3 encounter\$3 ideintify\$3 notic\$3 reveal\$3 (track\$3 ADJ down) uncover\$3 unmask\$3 discover\$3 (figur\$3 ADJ out) determin\$3 differentiat\$3 dig\$4 discriminat\$3 perceiv\$3 metrology)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 15:40

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S41	458129	S39 near S40	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 15:40
S42	7	S41 same (pre\$charg\$3 same reservoir)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 16:12
S43	43	S41 same (float\$10 same power same transfer\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 15:56
S45	10	S41 same (pre\$charg\$3 same ((backlog, basin, cistern, container, fund, holder, lake, nest egg*, pond, pool, receptacle, repository, reserve, source, spring, stock, stockpile, storage, store, supply, tank, reservoir, accumulation) near2 capacit\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 16:15
S48	174663	(protect\$3 adj (circuit device apparatus method system))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 16:56
S49	3	(protect\$3 adj (circuit device apparatus method system)) same (pre\$charg\$3 near5 reservoir near5 capacit\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 16:57
S50	3	(protect\$3 adj (circuit device apparatus method system)) same (pre\$charg\$3 near10 reservoir near10 capacit\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 16:57
S51	4	(protect\$3 adj (circuit device apparatus method system)) same (pre\$charg\$3 same reservoir same capacit\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 16:59

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S52	5	(protect\$3 adj (circuit device apparatus method system)) and (pre\$charg\$3 same reservoir same capacit\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 16:59
S53	93	"5909139" "6052019" "5808502" "4807104"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 17:09
S55	73	S53 and capacitor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 17:12
S56	25	S53 and capacitor and memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 17:13
S57	13	S53 and capacitor and memory and protect\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 17:14
S58	6	S53 and capacitor and memory and protect\$3 and fault	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 17:14
S59	6	S53 and capacitor and memory and protect\$3 and fault and detect\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 17:41
S60	3	S53 and capacitor and memory and protect\$3 and fault and detect\$3 and transfer\$4 same (float\$3 isolat\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 17:16

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S61	348	capacitor and memory and protect\$3 and fault and detect\$3 and transfer\$4 same (float\$3 isolat\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 17:16
S62	339	capacitor and memory and protect\$3 and fault and detect\$3 and transfer\$4 same (float\$3 isolat\$3) and power	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 17:17
S63	2026	capacitor and memory and protect\$3 and fault and detect\$3 and transfer\$4 and (float\$3 isolat\$3) and power	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 17:17
S64	2001	capacitor and memory and protect\$3 and fault and detect\$3 and transfer\$4 and (float\$3 isolat\$3) and power and control and signal	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 17:18
S66	76	capacitor and memory and protect\$3 and fault and detect\$3 and transfer\$4 and (float\$3 isolat\$3) and power and control and signal and pre\$charg\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 17:19
S67	74	capacitor and memory and protect\$3 and fault and detect\$3 and transfer\$4 and (float\$3 isolat\$3) and power and control and signal and pre\$charg\$3 and switch\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 17:20
S68	41	capacitor and memory and protect\$3 and fault and detect\$3 and transfer\$4 and (float\$3 isolat\$3) and power and control and signal and pre\$charg\$3 and switch\$3 and damag\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 17:36
S70	65	(capacitor same memory same protect\$3 same fault)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 17:38

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S71	1	(capacitor same memory same protect\$3 same fault same transfer\$4 same (float\$3 isolat\$3) same power)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 17:39
S72	4	(capacitor same memory same protect\$3 same fault same transfer\$4 same (float\$3 isolat\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 17:40
S73	34	S53 and memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 18:01
S74	25	S53 and memory and capacitor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 18:01
S75	19	S53 and memory and capacitor and transfer\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 18:06
S77	16	S53 and memory and capacitor and transfer\$4 not burn\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 18:07
S78	9	S53 and memory and capacitor and transfer\$4 and parallel not burn\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 18:07
S79	8	"5483142"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 19:00

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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L27	1170	361/103.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/09 22:24